

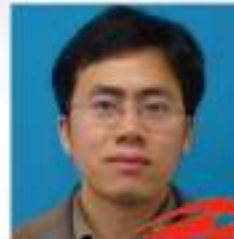
# Enhanced and Continuous Electrostatic Carrier Density Control for Mottronics



Isao H. Inoue



S. Asanuma



P. H. Xiang



A. Sawa



A. Eyvazov



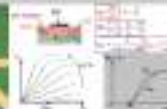
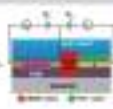
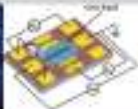
C. Panagopoulos



--- National Institute of Advanced Industrial Science & Technology (AIST), Japan



--- Nanyang Technological University, Singapore



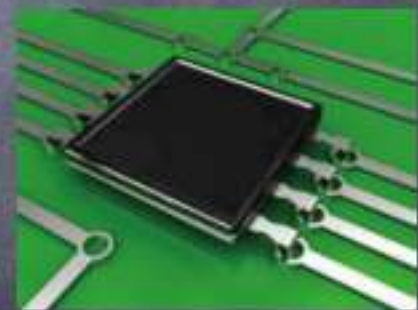
# What are expected for Mott FET



I. Overcome the “Miniaturisation Limit”



II. High Speed but Low Power Consumption



III. Multi-functionality

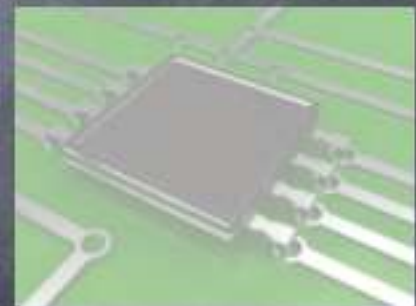


# What are expected for Mott FET

I. Overcome the “Miniaturisation Limit”



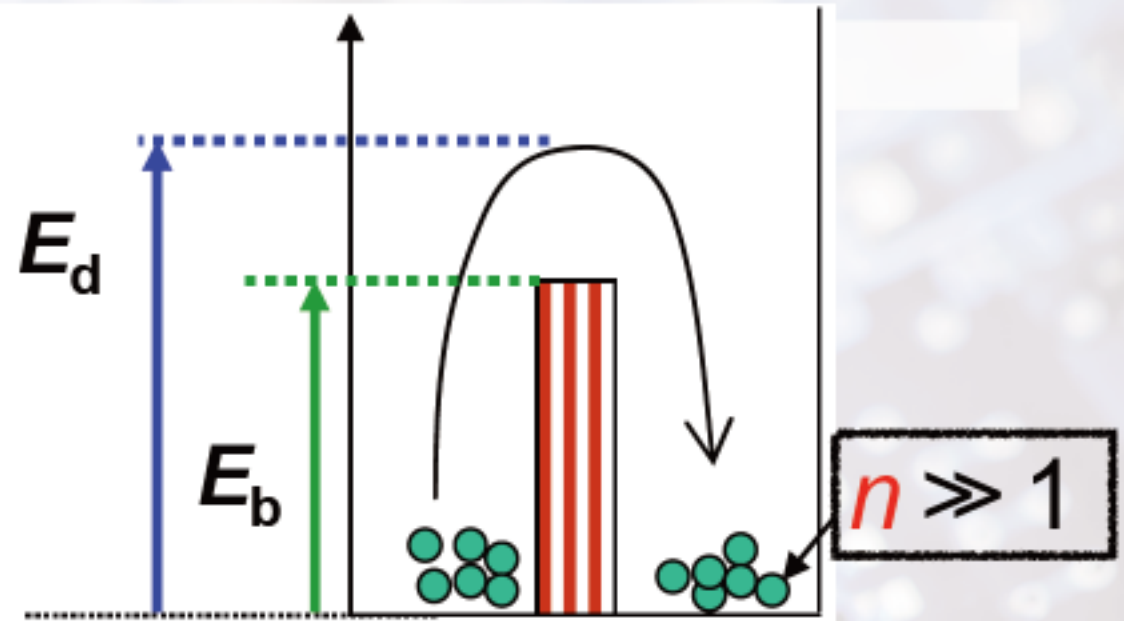
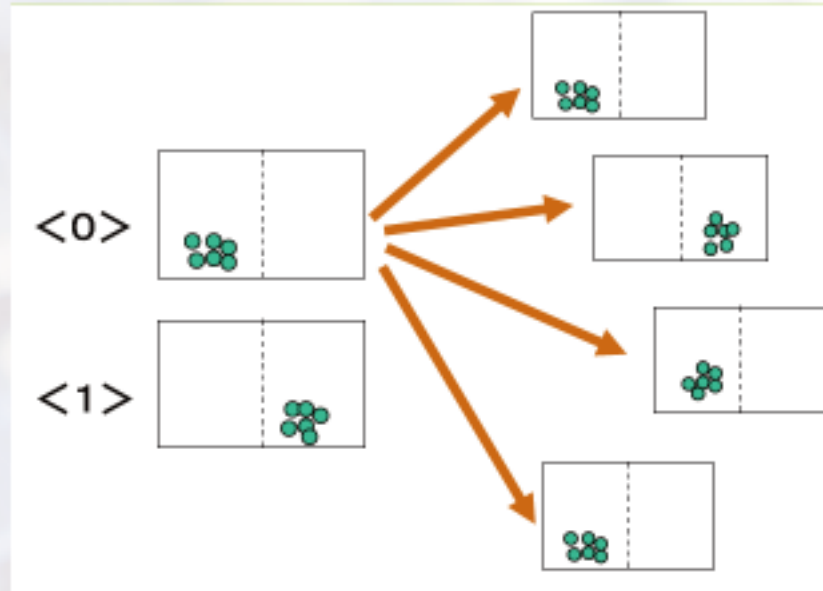
II. High Speed but Low Power Consumption



III. Multi-functionality



# Switching energy of a transistor



each  $\langle 0 \rangle$  and  $\langle 1 \rangle$  must be clearly distinguished and controlled

*switching energy*

$$nE_d \gg 100k_B T \sim 2.6eV$$

$$E_d = E_b + \text{dissipation}$$

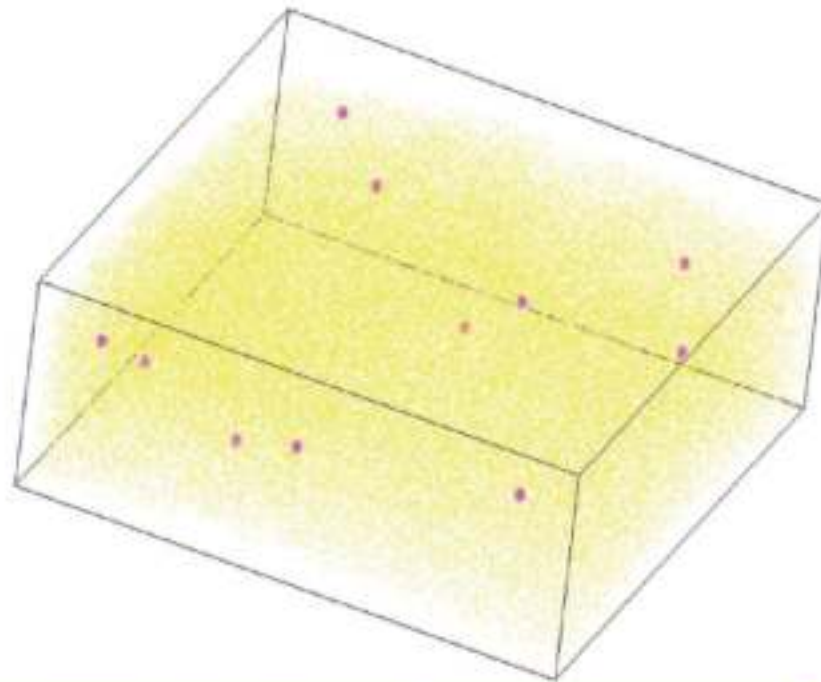
$100k_B T$  = thermal noise limit

# MOSFET in ~2020

Figure 3 Three-dimensional atom probe (3DAP) reconstruction of 11 boron dopants (large pink dots) in silicon (small yellow dots). The analysed volume is 20 nm × 20 nm × 5 nm. The 3DAP technique can detect the position and chemical species of dopant atoms within a small volume, but it cannot yet provide crucial information about their electrical activity.

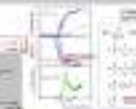
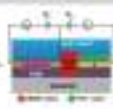
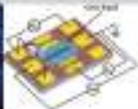
Image courtesy of Alfred Cerezo (University of Oxford) and David Larson (Seagate Technology).

M. R. Castell et al.,  
Nature Materials 2,  
129 (2003)

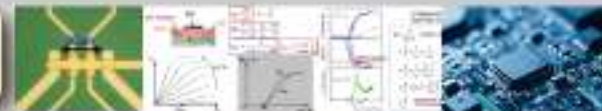
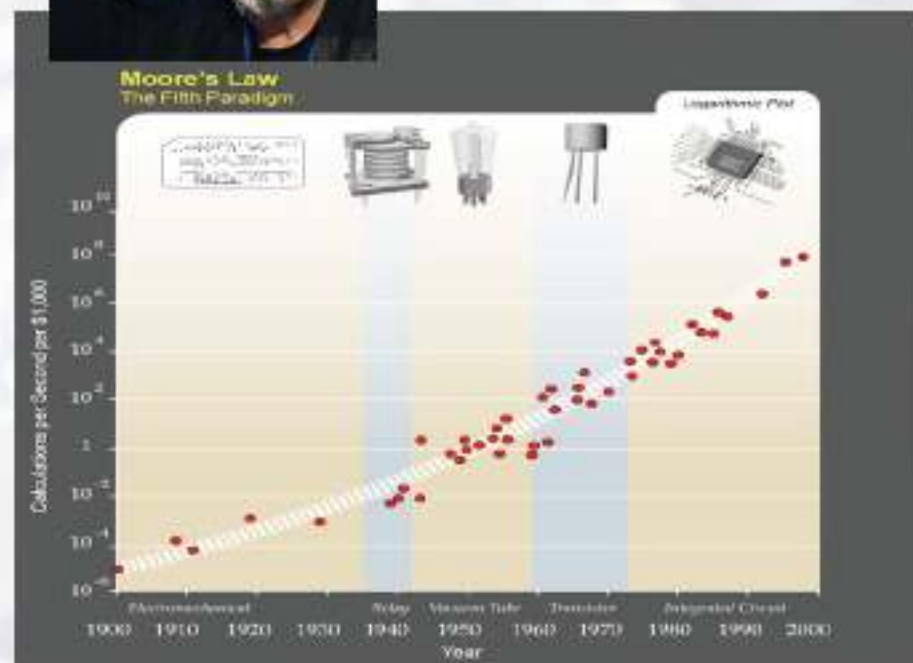
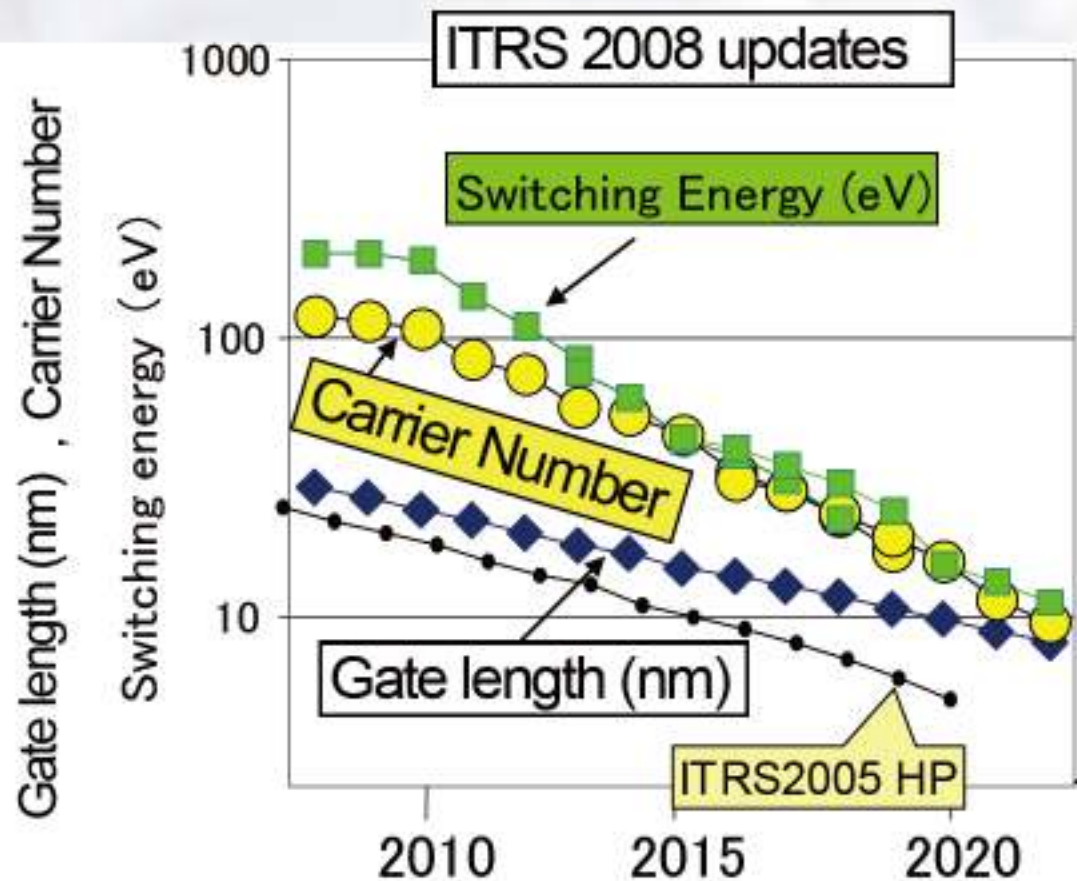


**20nm device has only 11 carriers.**

*switching energy*  
 $nE_d \sim 100k_B T \sim 2.6eV$



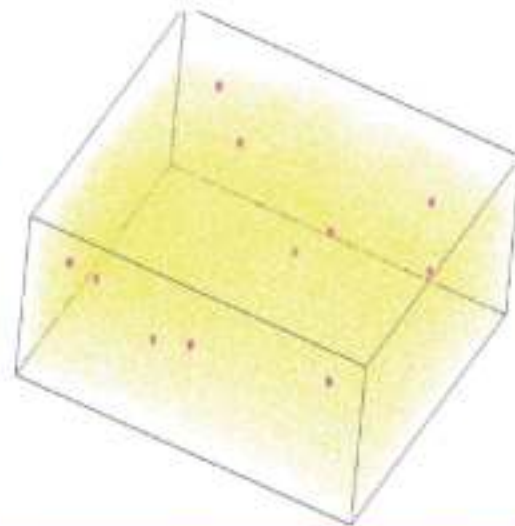
# Miniaturisation limit



# Is Mott FET better?

## MOSFET in 2020

Figure 3 Three-dimensional atom probe (3DAP) reconstruction of 11 boron dopants (large pink dots) in silicon (small yellow dots). The analysed volume is 20 nm × 20 nm × 5 nm. The 3DAP technique can detect the position and chemical species of dopant atoms within a small volume, but it cannot yet provide crucial information about their electrical activity.



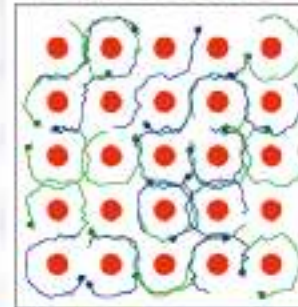
M. R. Castell et al.,  
Nature Materials 2,  
129 (2003)

Image courtesy of Alfred Conso  
(University of Oxford) and David Lanson  
(Singapore Technology).

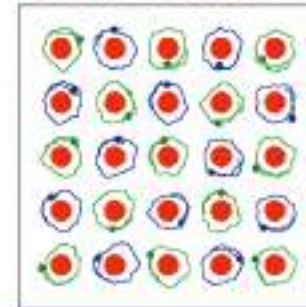
**20nm device has only 11 carriers.**  
**Almost the end of the minituarisation**

## Mott FET

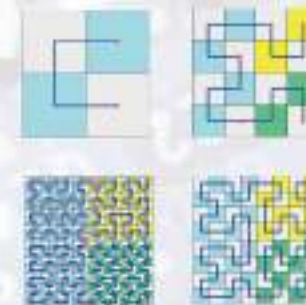
On State



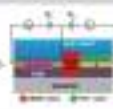
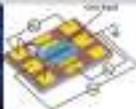
Off State



**20nm device has more than 100,000 carriers.** Carrier density is independent of device sizes.



Analogous with "self-similar" Peano curves



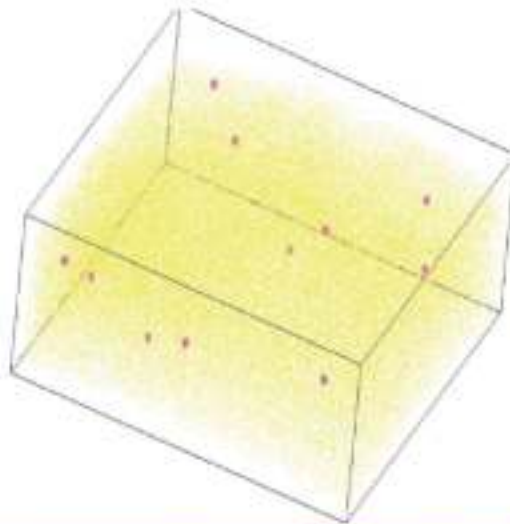
# Is Mott FET better?

## MOSFET in 2020

Figure 3 Three-dimensional atom probe (3DAP) reconstruction of 11 boron dopants (large pink dots) in silicon (small yellow dots). The analysed volume is 20 nm × 20 nm × 5 nm. The 3DAP technique can detect the position and chemical species of dopant atoms within a small volume, but it cannot yet provide crucial information about their electrical activity.

Image courtesy of Alfred Conroy (University of Oxford) and David Lanson (Singapore Technology).

M. R. Castell et al.,  
Nature Materials 2,  
129 (2003)



**20nm device has only 11 carriers.**  
**Almost the end of the minituarisation**

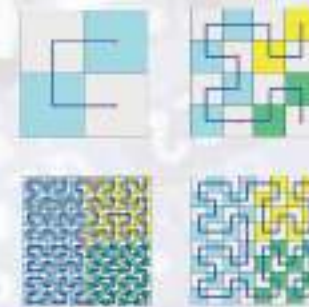
## Mott FET

On State

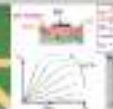
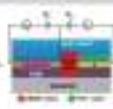
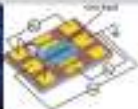
Off State



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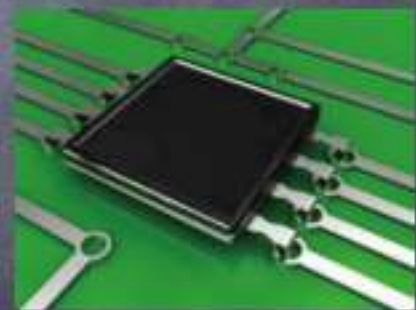


# What are expected for Mott FET

I. Overcome the “Miniaturisation Limit”



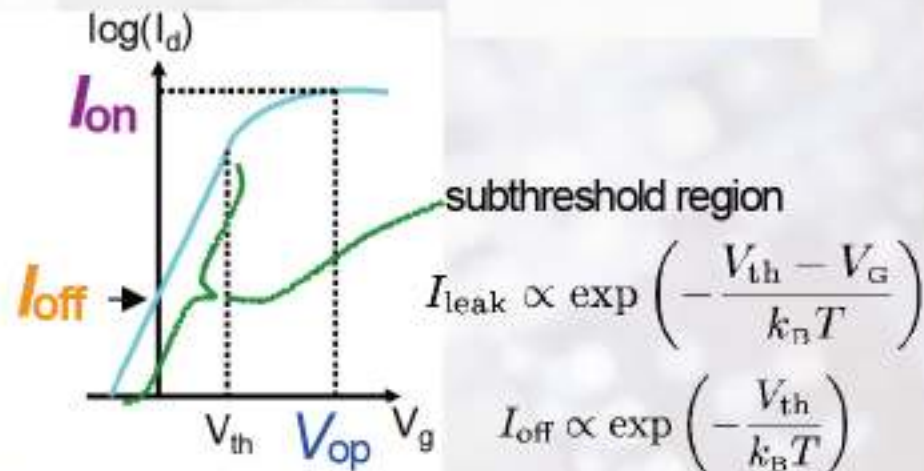
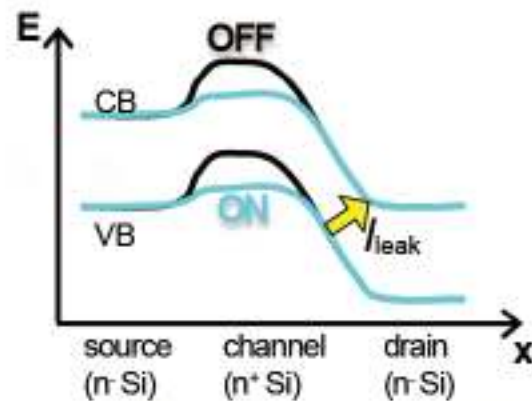
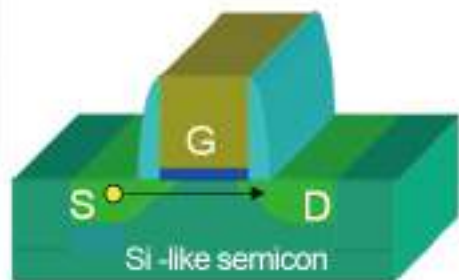
II. **High Speed but Low Power Consumption**



III. Multi-functionality



# Sophisticated Si FET



**Total Power Consumption** = Operating Power + Standby Power

$$\propto f \cdot C \cdot V_{op}^2 \quad \propto I_{off} \cdot V_{op}$$

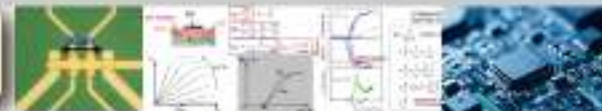
**Switching Speed**  $\propto \frac{I_{on}}{C V_{op}}$

**requires**

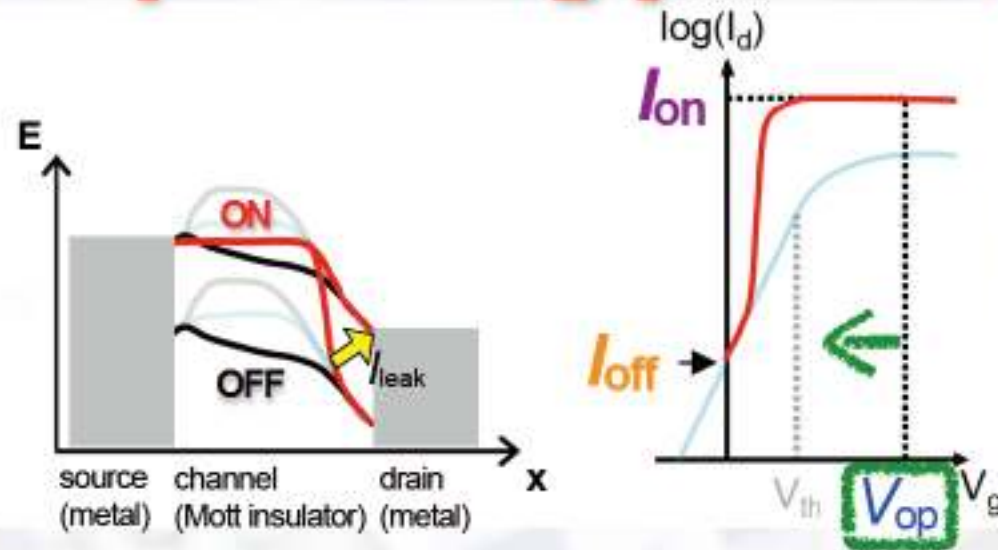
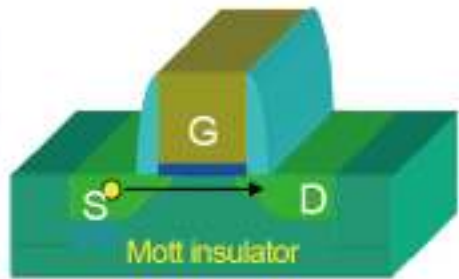
$C V_{op}^2 \downarrow$  more than  $f \uparrow$

$V_{op} \downarrow$  and  $I_{off} \downarrow$

$C V_{op} \downarrow$  and  $I_{on} \uparrow$



# Mott FET prototype at present



**Total Power Consumption** = Operating Power + Standby Power  
 $\propto f \cdot C \cdot V_{op}^2$        $\propto I_{off} \cdot V_{op}$

**Switching Speed**  $\propto \frac{I_{on}}{C V_{op}}$



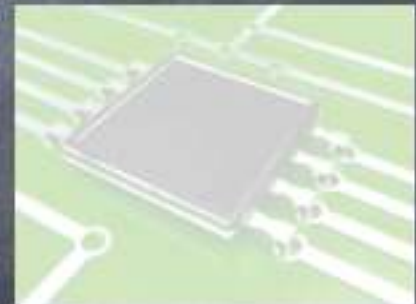
- $C V_{op}^2 \downarrow$  more than  $f \uparrow$
- $V_{op} \downarrow$  and  $I_{off} \downarrow$
- $C V_{op} \downarrow$  and  $I_{on} \uparrow$

# What are expected for Mott FET

I. Overcome the “Miniaturisation Limit”



II. High Speed but Low Power Consumption



**III. Multi-functionality**



# Multi-functionality

Action of the electromagnetic field

$$S_0 = \frac{1}{8\pi} \int d^3x dt \left( \epsilon \mathbf{E}^2 - \frac{1}{\mu} \mathbf{B}^2 \right)$$

Adding this scalar term does **not affect** the Maxwell's equations

$$S_\theta = \left( \frac{\theta}{2\pi} \right) \left( \frac{\alpha}{2\pi} \right) \int d^3x dt \mathbf{E} \cdot \mathbf{B} \quad \alpha = \frac{e^2}{hc}$$

Then, the following relations are obtained

$$\mathbf{D} = \mathbf{E} + 4\pi \mathbf{P} - 2\alpha P_3 \mathbf{B} \quad \text{physics}$$

$$\mathbf{H} = \mathbf{B} - 4\pi \mathbf{M} + 2\alpha P_3 \mathbf{E}$$

$$P_3(x) = \theta(x)/2\pi$$

$S_\theta$  is a kind of spin-orbit interaction

Trends

Spintronics without magnetism

David Awschalom

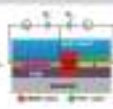
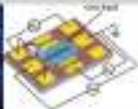
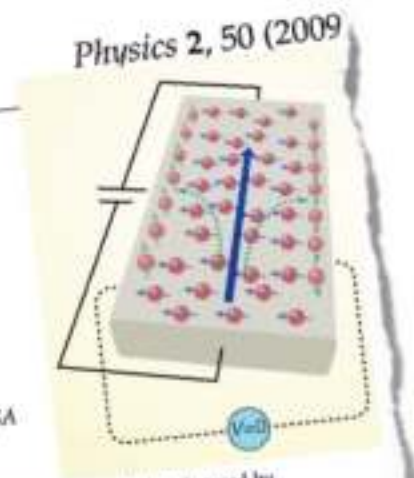
Department of Physics, University of California, Santa Barbara, CA 93106, USA

Nitin Samarth

Department of Physics, The Pennsylvania State University, University Park, PA 16802, USA

Published June 15, 2009

The spin-orbit effect is at the heart of efforts to merge spintronics—where information is carried and stored by spin—rather than by charge—with semiconductor technology.



# Obstacles to the Mott FET

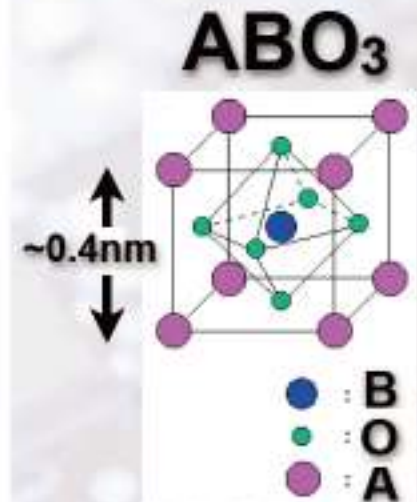
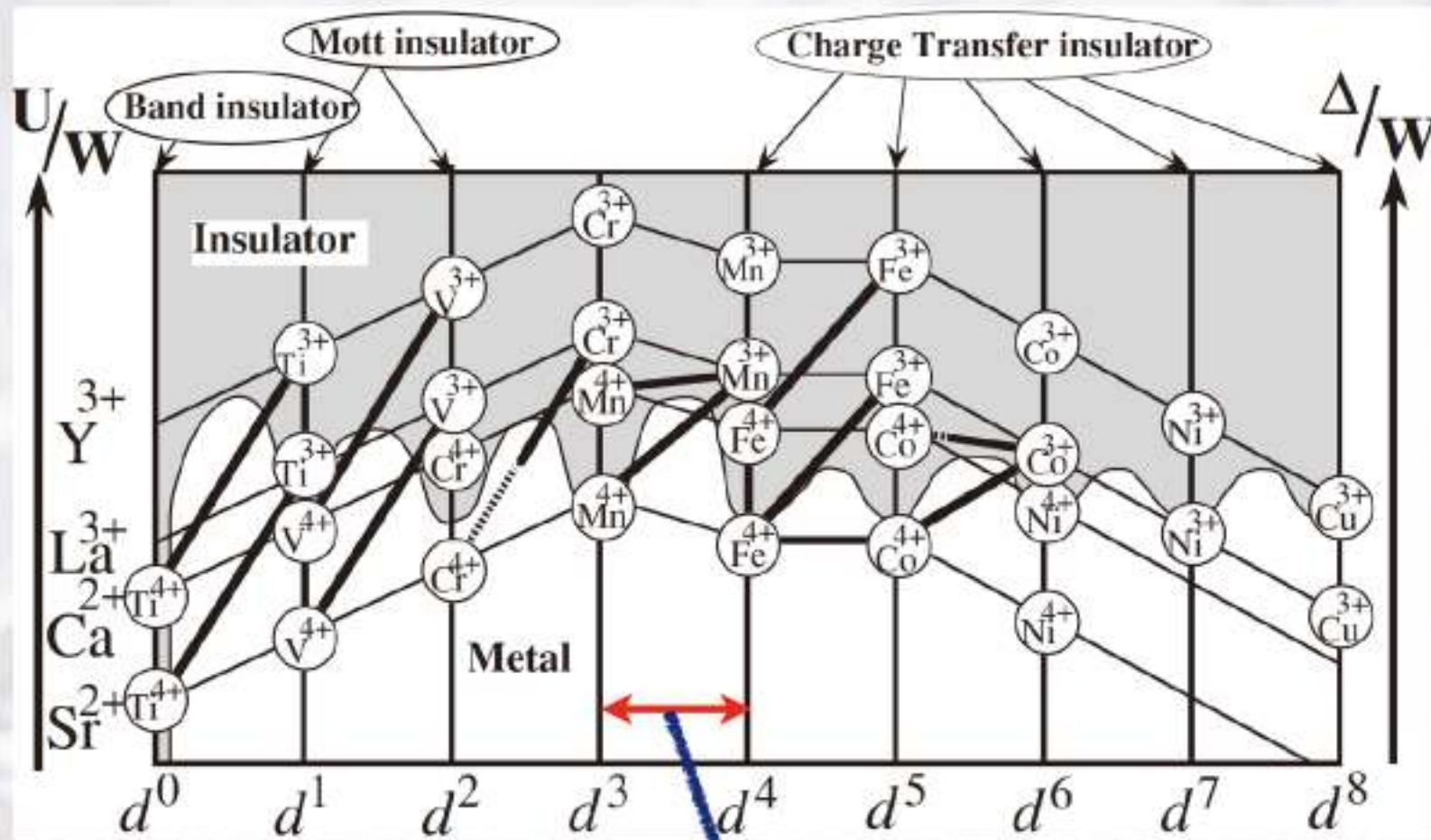
I. **Huge** amount of **carrier density** control



II. Correlated materials  
 $\approx$  ionic crystals = **Defects** form easily.



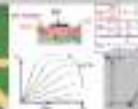
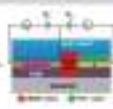
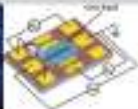
# Huge carrier density control



I. H. Inoue,  
Semicond. Sci. Technol.  
20, 1 (2005)

$$\sim 1.6 \times 10^{22} \text{ cm}^{-3}$$

$$= \sim 6 \times 10^{15} \text{ cm}^{-2} / 4 \text{ nm}$$



# Obstacles to the Mott FET

I. Huge amount of carrier density control



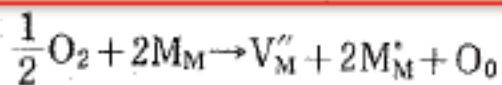
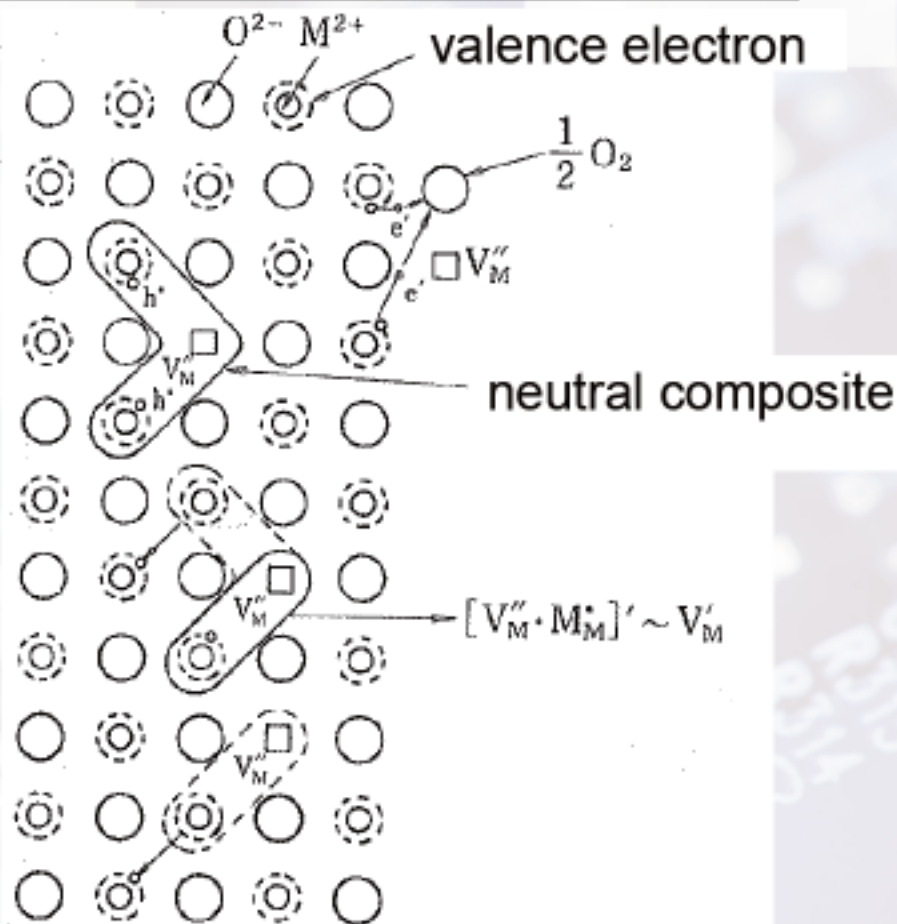
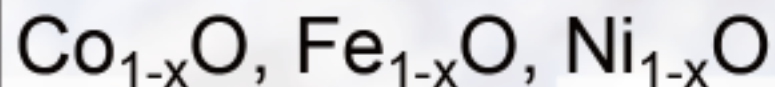
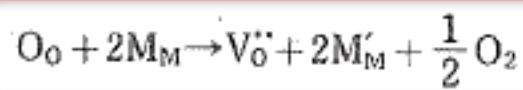
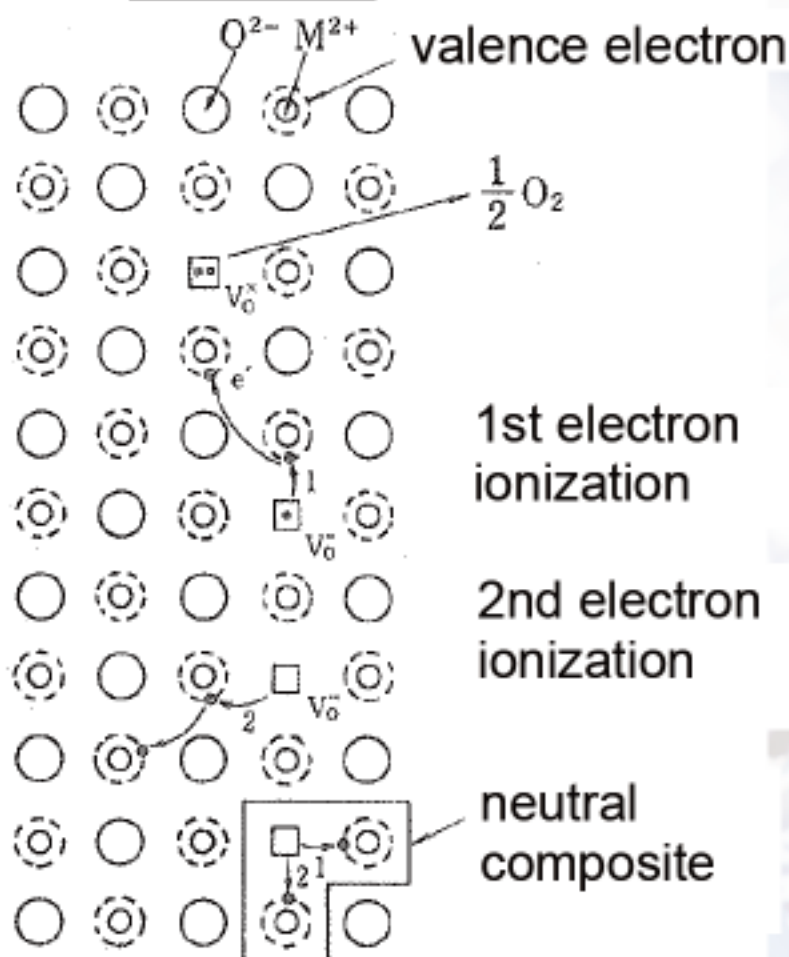
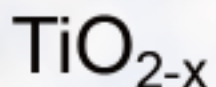
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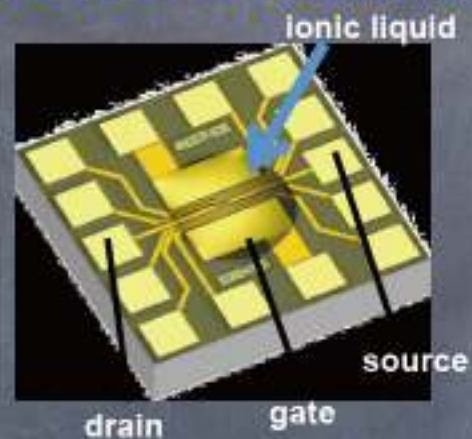


# Defects in transition-metal oxides



# Candidates of the Mott FET

## I. Electric double layer FET

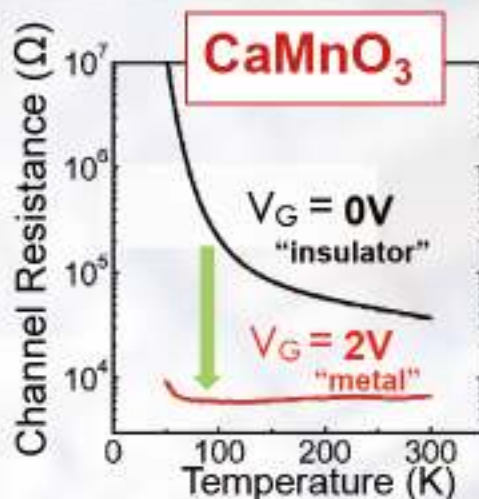
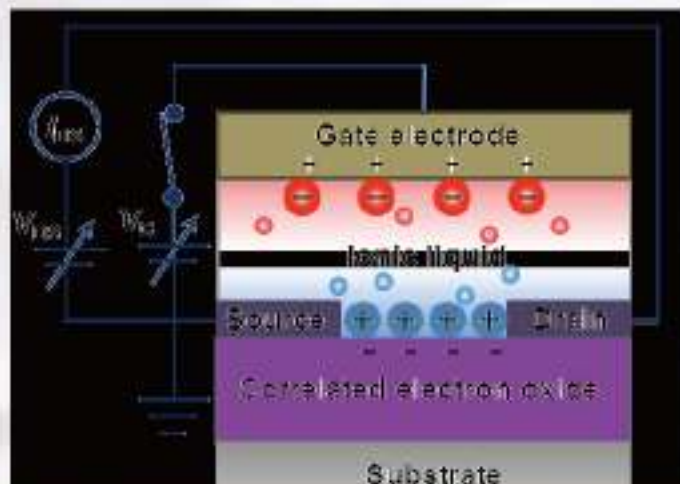


## II. Hybrid gate-insulator FET

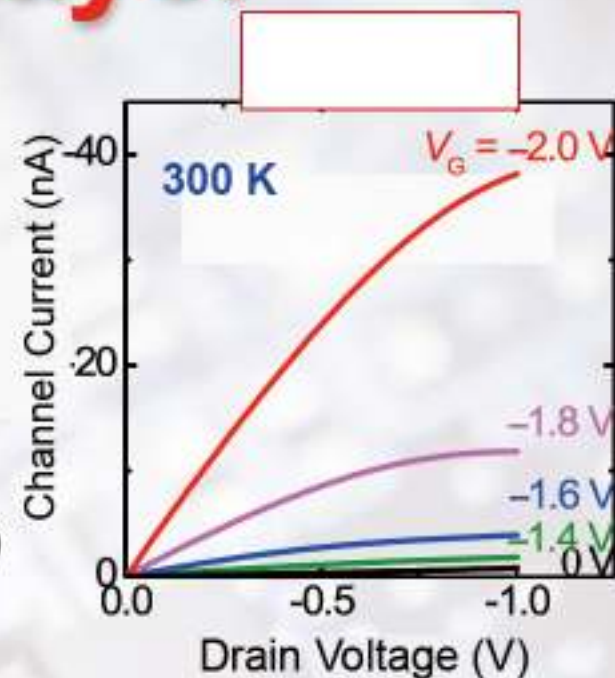
high-k oxide

Parylene-C

# Electric Double Layer



P.H.Xiang et al., Adv. Mat. **23**, 5822 (2011)

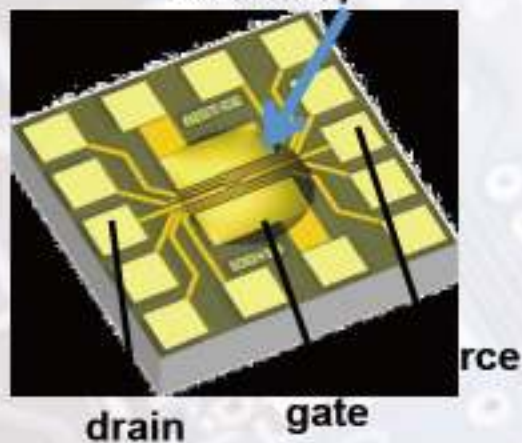


P.H.Xiang et al., submitted to APL

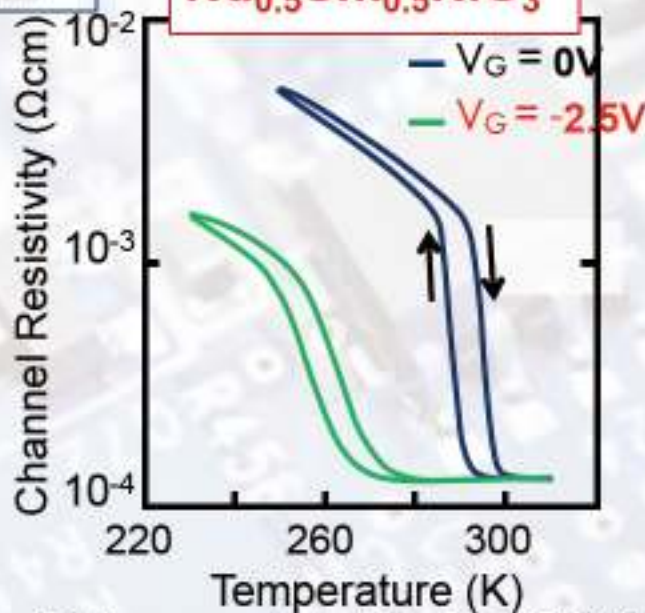
$$10 \mu\text{F}/\text{cm}^2 \times 2 \text{ V} = 1.25 \times 10^{14} \text{ e} / \text{cm}^2$$

ionic liquid

[N,N-diethyl-N-(2-methoxyethyl)-  
N-methylammonium tetrafluoroborate  
DEME-BF4]

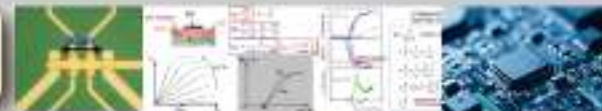


**Nd<sub>0.5</sub>Sm<sub>0.5</sub>NiO<sub>3</sub>**

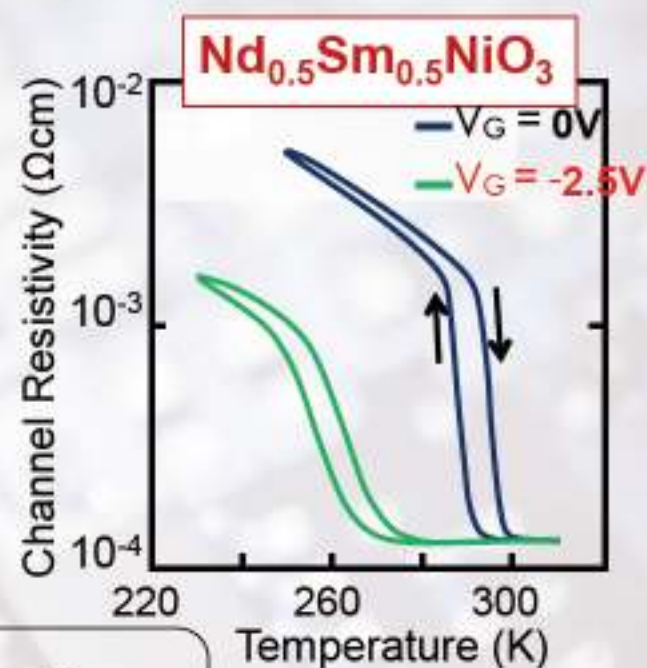
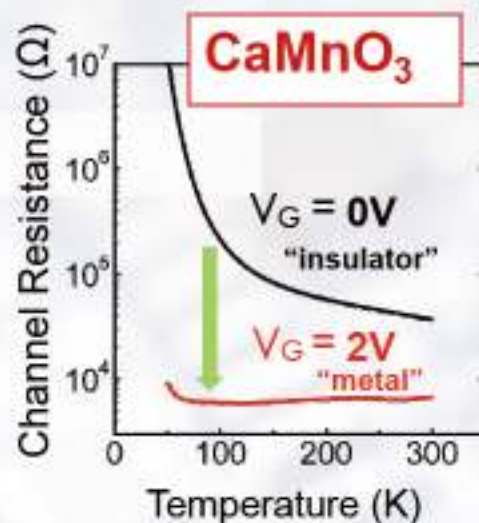
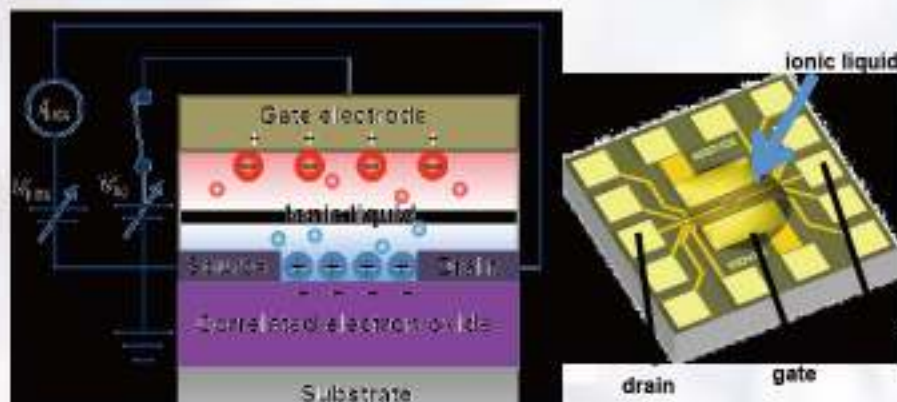


S. Asanuma et al., APL. **97**, 142110 (2010)

Very Promising!!



# Electric Double Layer



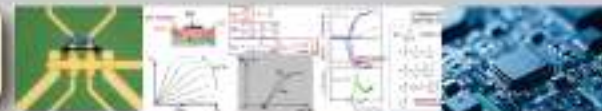
$$10 \mu\text{F}/\text{cm}^2 \times 2 \text{ V} = 1.25 \times 10^{14} \text{ e} / \text{cm}^2$$

$$\text{Total Power Consumption} = \text{Operating Power} + \text{Standby Power}$$

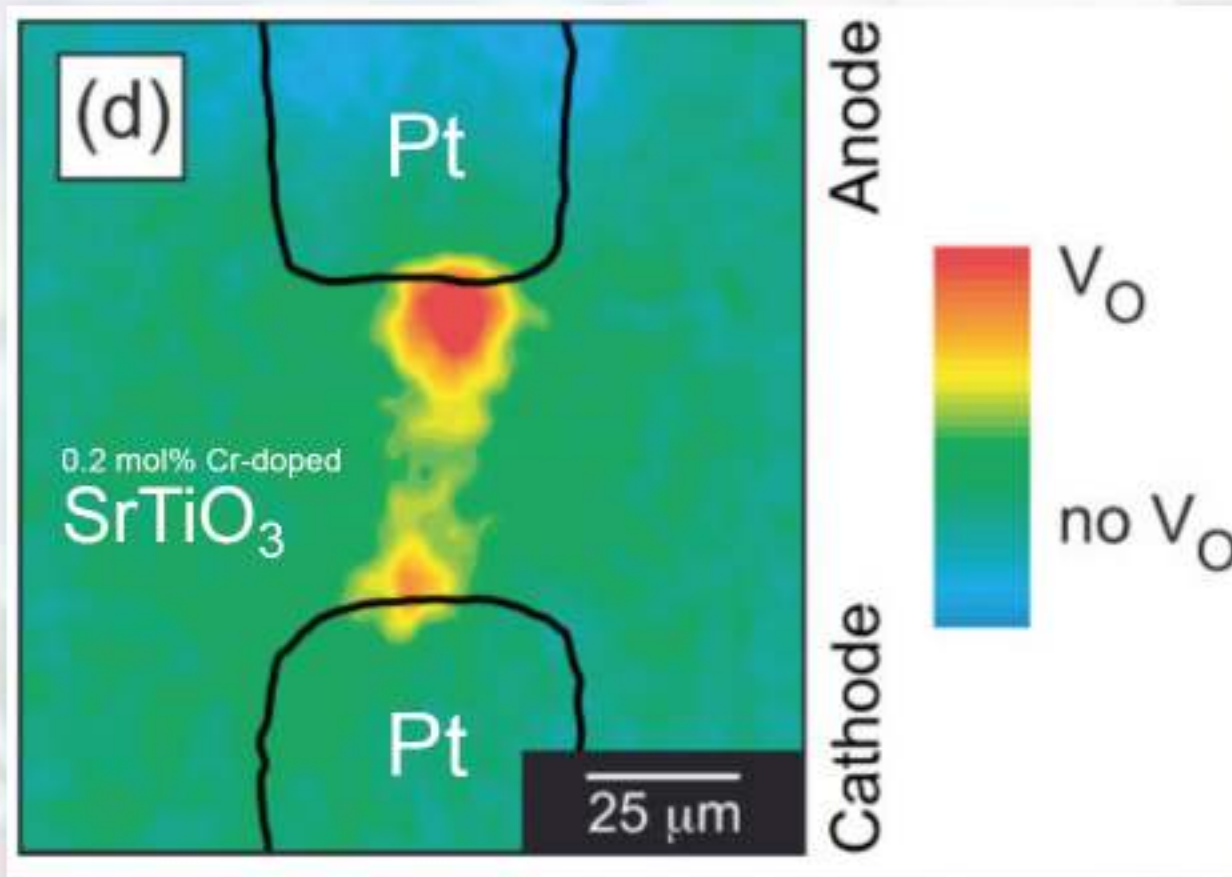
$$\propto f \cdot C \cdot V_{\text{op}}^2 \quad \propto I_{\text{off}} \cdot V_{\text{op}}$$

$$\text{Switching Speed} \propto \frac{I_{\text{on}}}{C \cdot V_{\text{op}}}$$

- ▶ **Miniaturisation is difficult...**
- ▶ C is too large compared to  $I_{\text{on}}/V_{\text{op}}$ ...
- ▶ Continuous control of  $n$  is difficult...
- ▶ Moreover...



# Oxygen vacancies are created

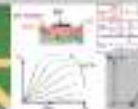
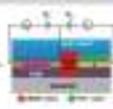
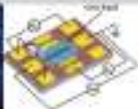


By applying  $10^5 \text{V/cm}$   
for about 30 min



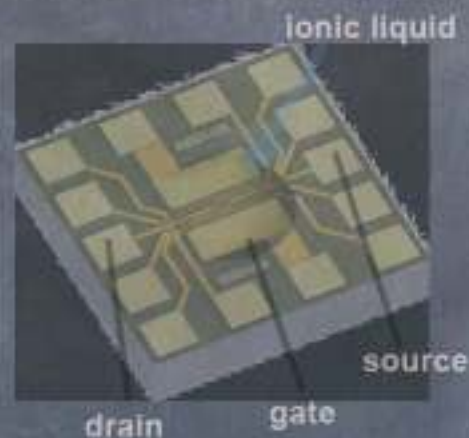
**Oxygen vacancies** are created,  
and distributed in the channel,  
and form a **metallic path**.

M. Janousch et al., Adv. Mat. **19**, 2232 (2007)



# Candidates of the Mott FET

## I. Electric double layer FET



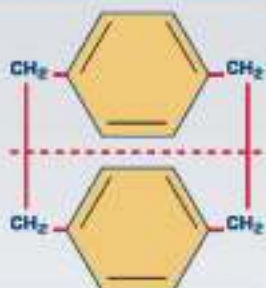
## II. **Hybrid** gate-insulator FET



# Parylene to protect oxide surface

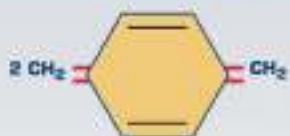
## Vaporization

Dimer  
di-para-xylylene  
150°C, 1.0 torr



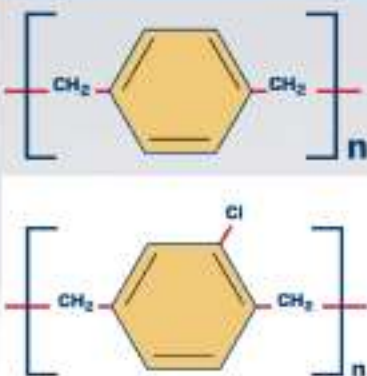
## Pyrolysis

Monomer  
di-para-xylylene  
680°C, 0.5 torr



## Deposition

Polymer  
poly(para-xylylene)  
25°C, 0.1 torr



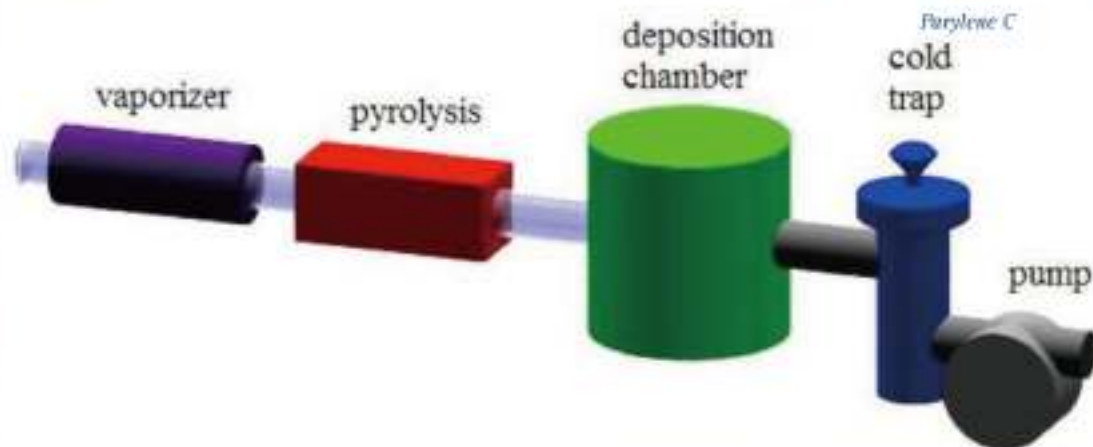
Vacuum Deposition  
Yields Conformal,  
Even Coating

## PARYLENE COATING



Liquid Coatings  
Conform to Meniscus  
and Surface Tension  
Forces

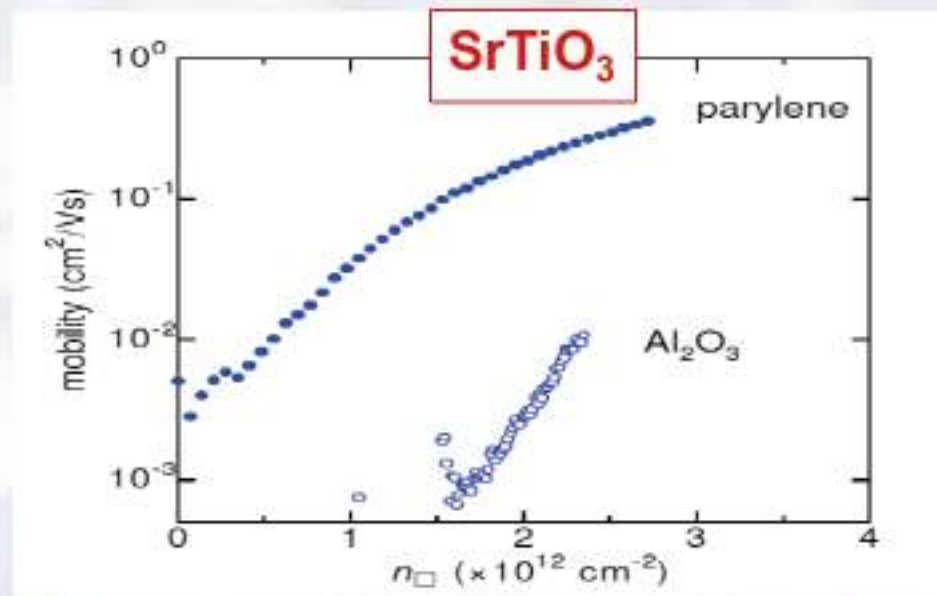
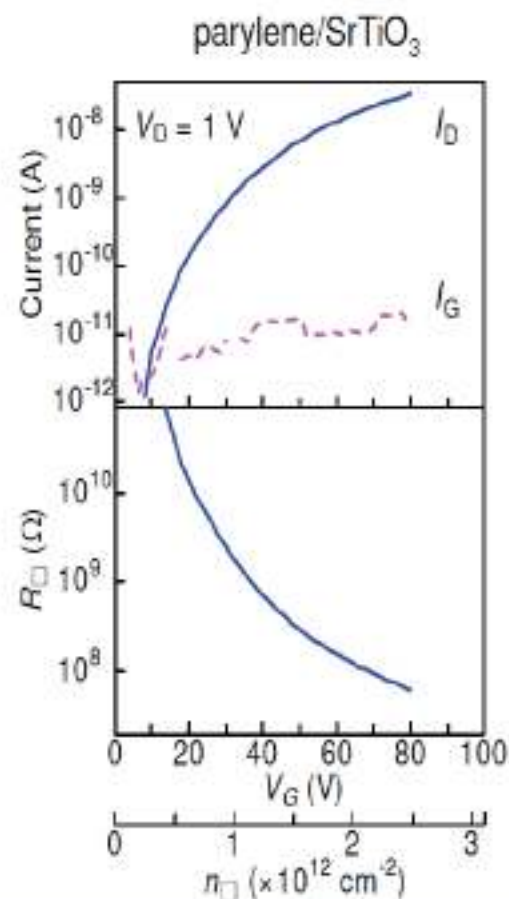
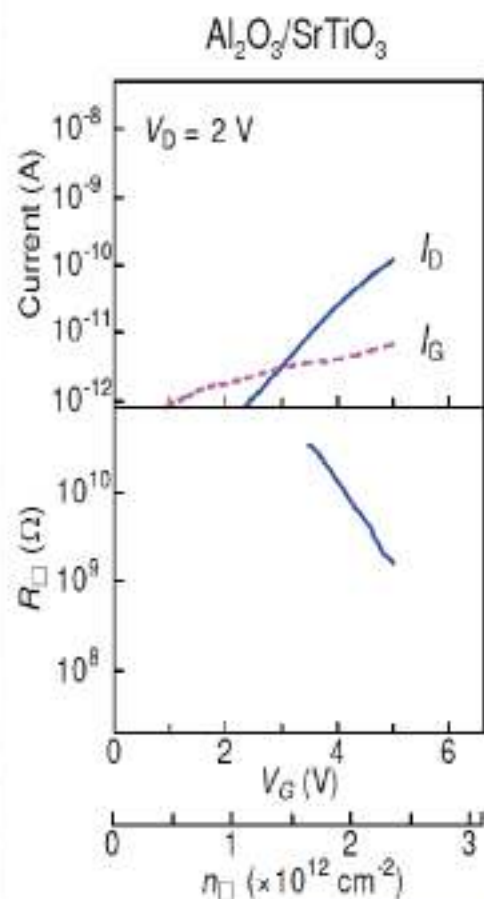
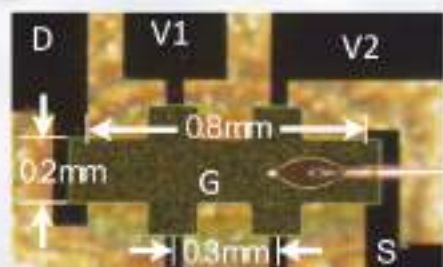
## CONVENTIONAL COATING



Creation of oxygen vacancies is suppressed.  
Channel is kept clean.

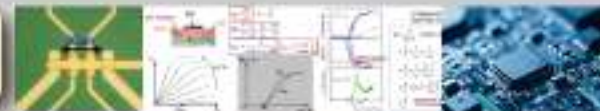


# Parylene as a gate insulator



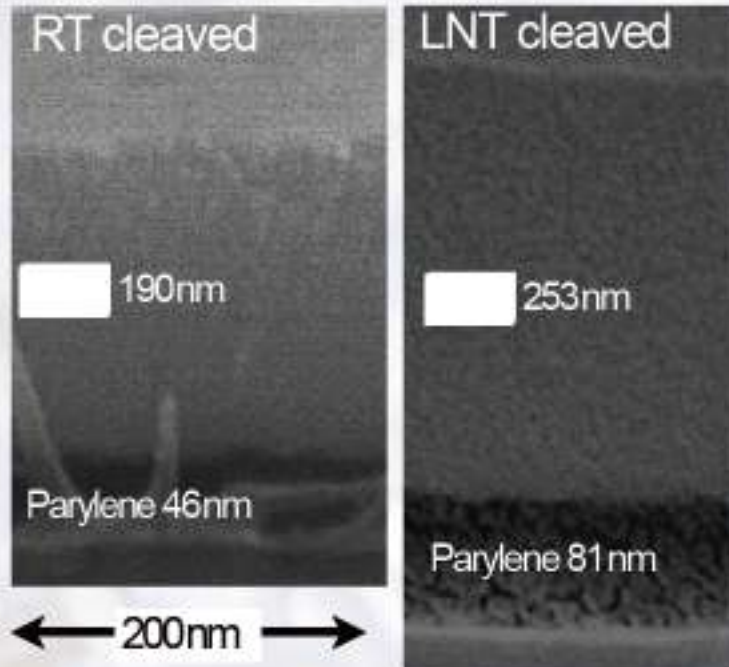
Using Parylene for the gate insulator, mobility is drastically enhanced.

But carrier density is too small...

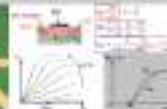
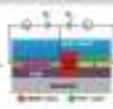
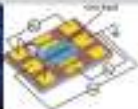




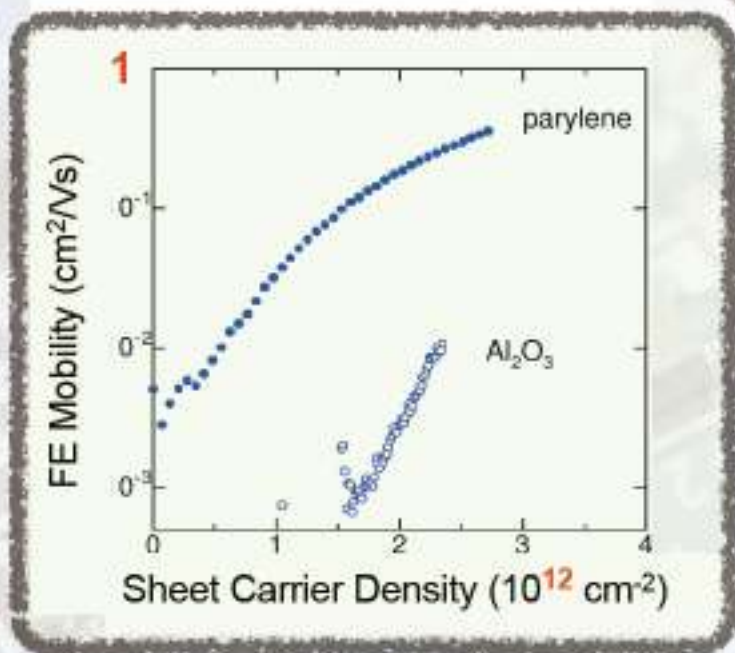
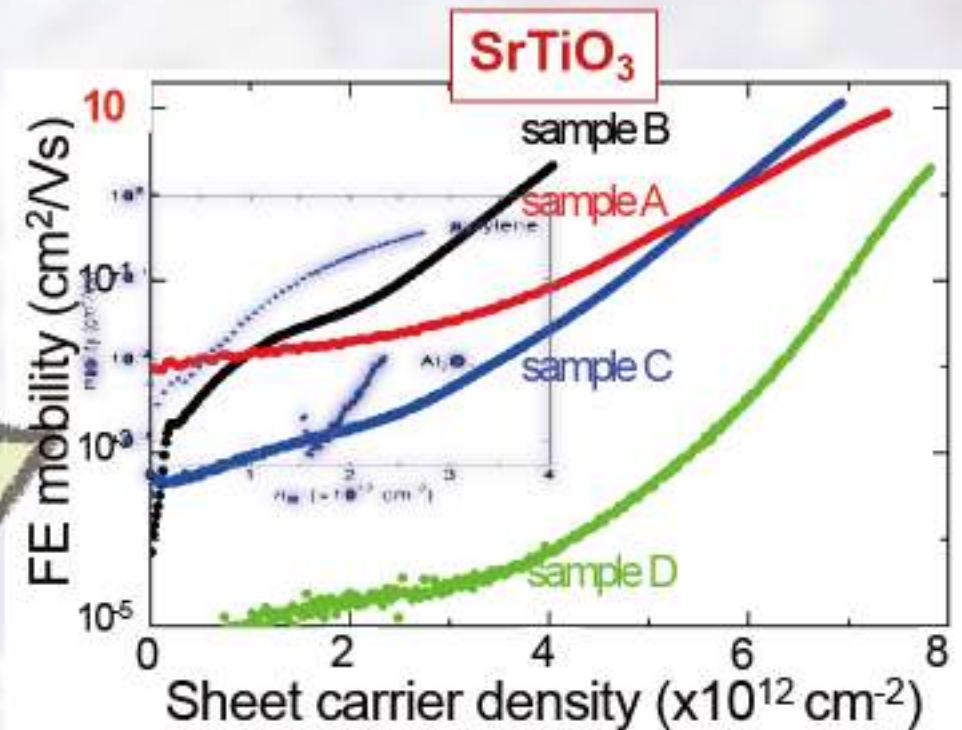
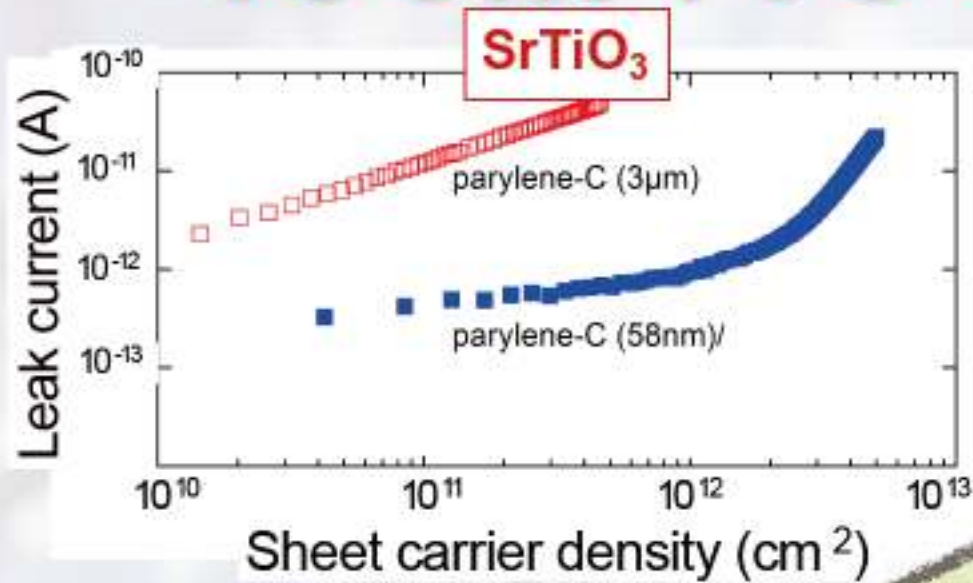
# Bilayer Gate Insulator



deposition of  
on the top of **Parylene-C** ( $\epsilon=3.2$ )

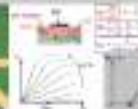
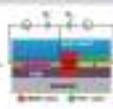
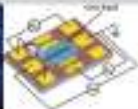


# 10 cm<sup>2</sup>/Vs & ~10<sup>13</sup> cm<sup>-2</sup>



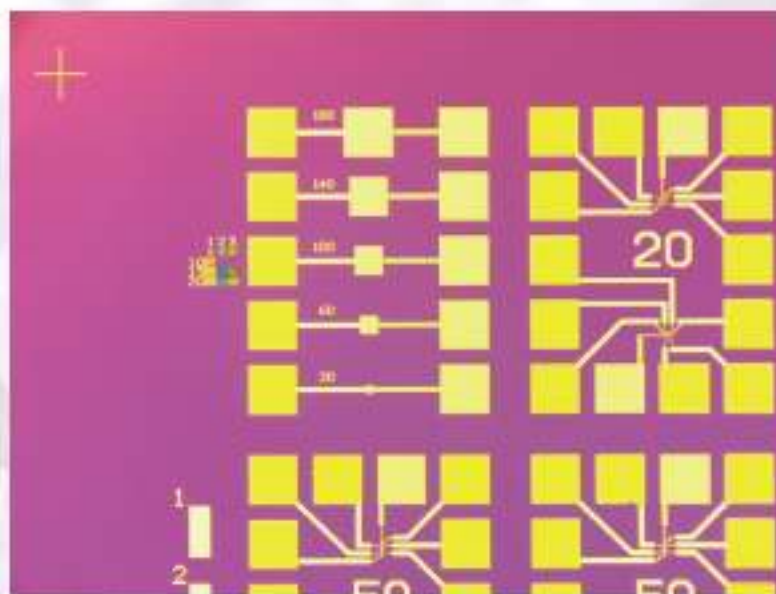
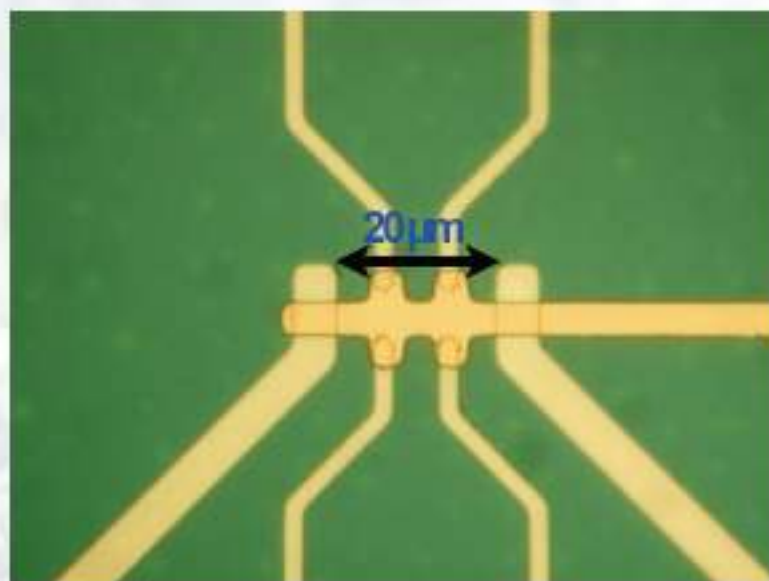
Parylene/ bilayer gate insulator gives **extremely large mobility of 10 cm<sup>2</sup>/Vs.**

Carrier density reaches **~10<sup>13</sup> /cm<sup>-2</sup> !!**

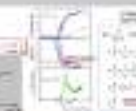
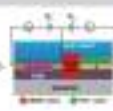
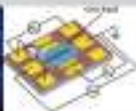


# Versatile to photolithography

- ▶ Oxide **surface** is first covered by Parylene-C and is **kept clean**
- ▶ Parylene-C can be as thin as 5-20nm



Prototype device is coming soon !!



# Summary

## Expected for future transistors

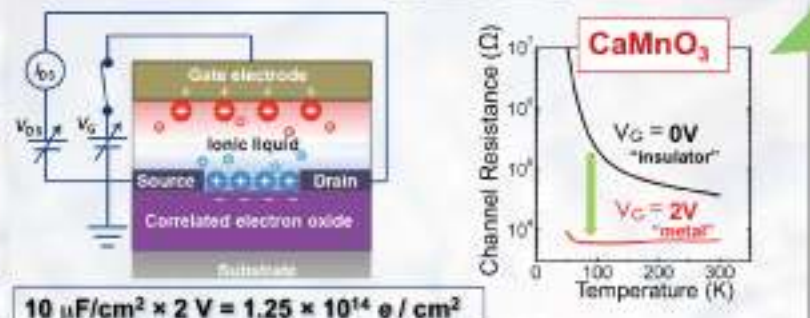
- I. Overcome the "Miniaturisation Limit"
- II. **High Speed** but **Low Power** Consumption
- III. **Multi-functionality**

**Obstacles to the Mott FET**

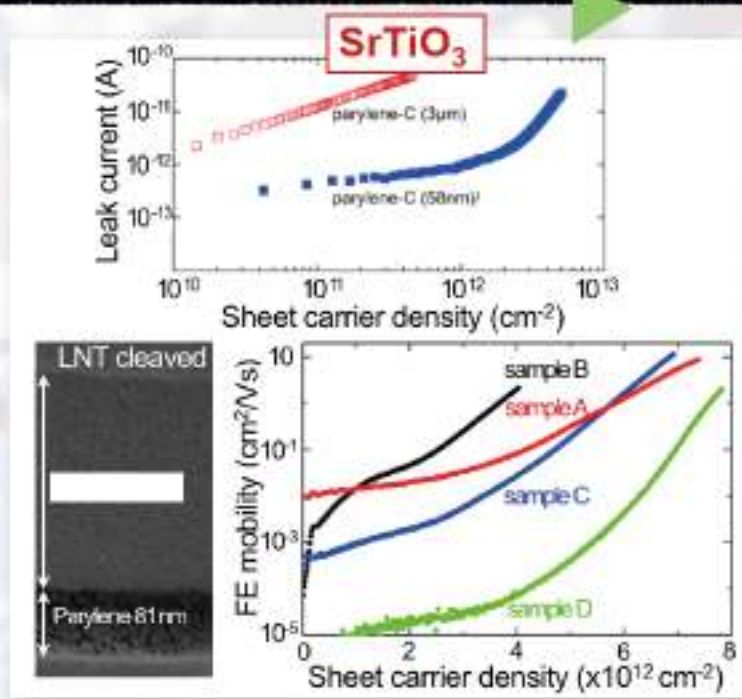
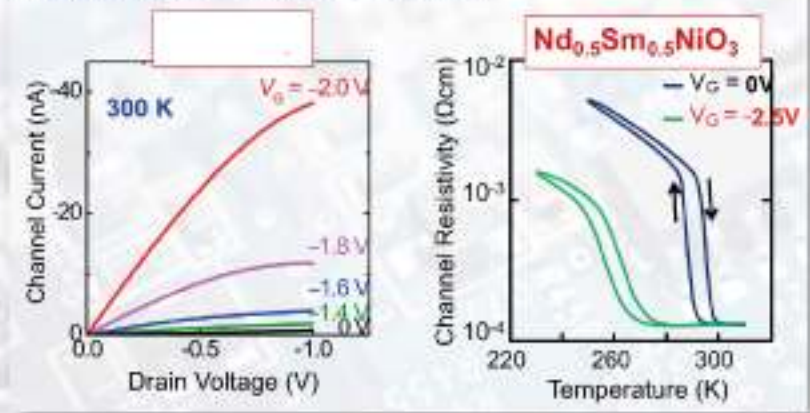
- I. **Huge** amount of **carrier density control**
- II. Correlated materials  $\approx$  ionic crystals = **Defects** form easily.

## Candidates of the Mott FET

- I. **Electric double layer FET**
- II. **Hybrid gate-insulator FET**



$$10 \mu\text{F}/\text{cm}^2 \times 2 \text{ V} = 1.25 \times 10^{14} \text{ e} / \text{cm}^2$$

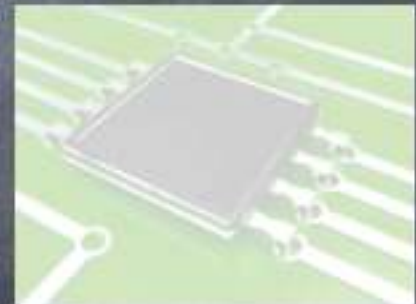


# In addition, ...

I. Overcome the “Miniaturisation Limit”



II. High Speed but Low Power Consumption

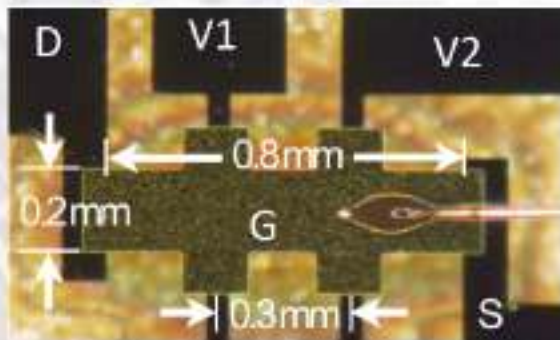
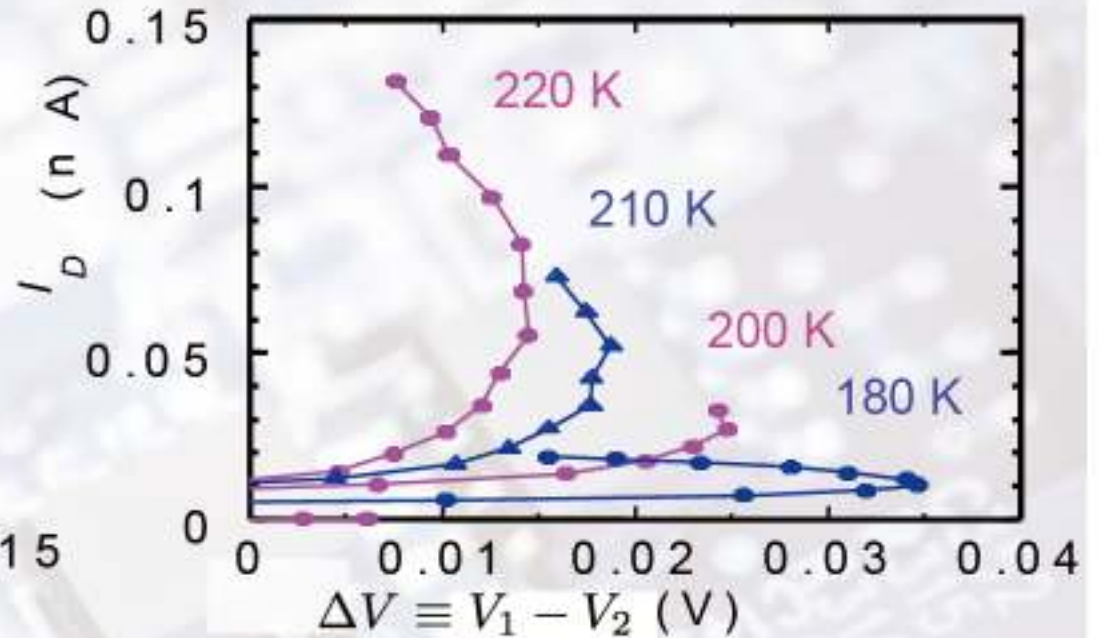
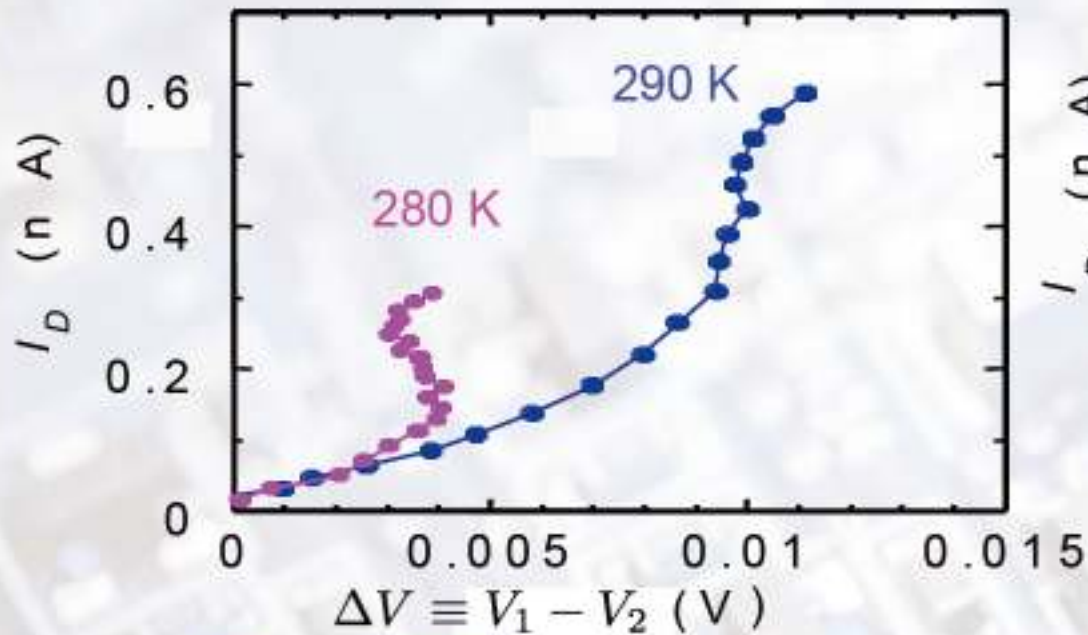


III. **Multi-functionality**



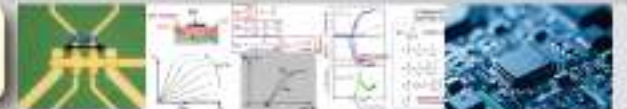
# S-shape I-V Curves

## Parylene/SrTiO<sub>3</sub>



Hardly seen in Al<sub>2</sub>O<sub>3</sub>/SrTiO<sub>3</sub>

Al<sub>2</sub>O<sub>3</sub>/SrTiO<sub>3</sub> has some amount of carriers from the first



# Filamentary Path Formation

## Specific Negative Resistance in Solids

By B. K. RIDLEY

Solid State Physics Division, Mullard Research Laboratories, Redhill, Surrey

MS. received 19th August 1963

Proc. Phys. Soc. 82, 954 (1963)

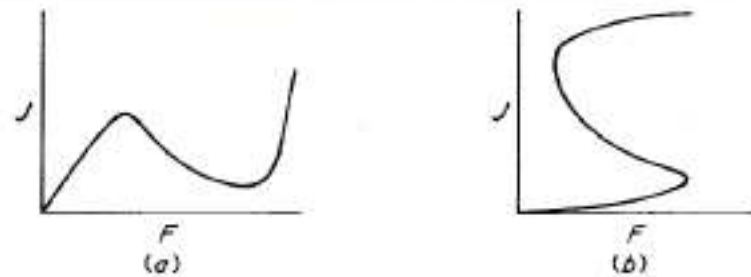


Figure 1. The two types of differential negative resistance: (a) voltage-controlled; (b) current-controlled.

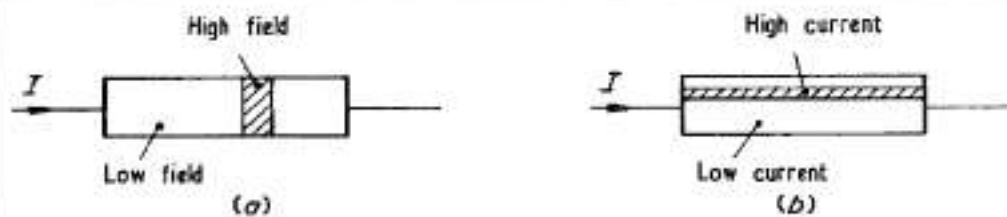


Figure 2. Domain and filament formation: (a) domain; (b) filament.



"Nonlinear..."  
by E. Scholl,  
Cambridge Univ. Press  
(2001)

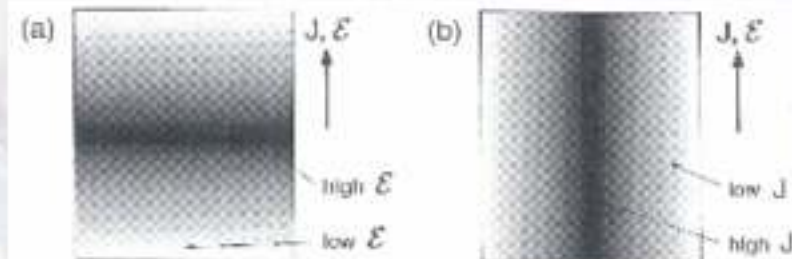
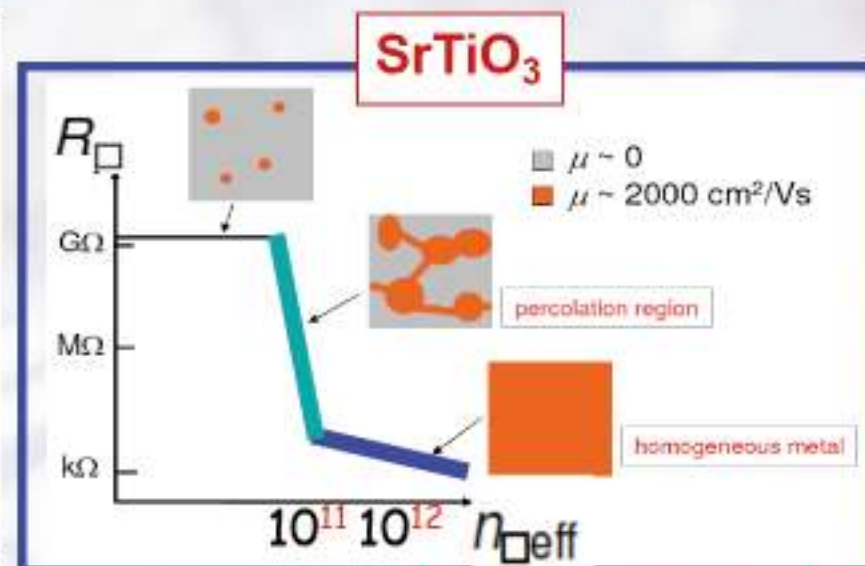
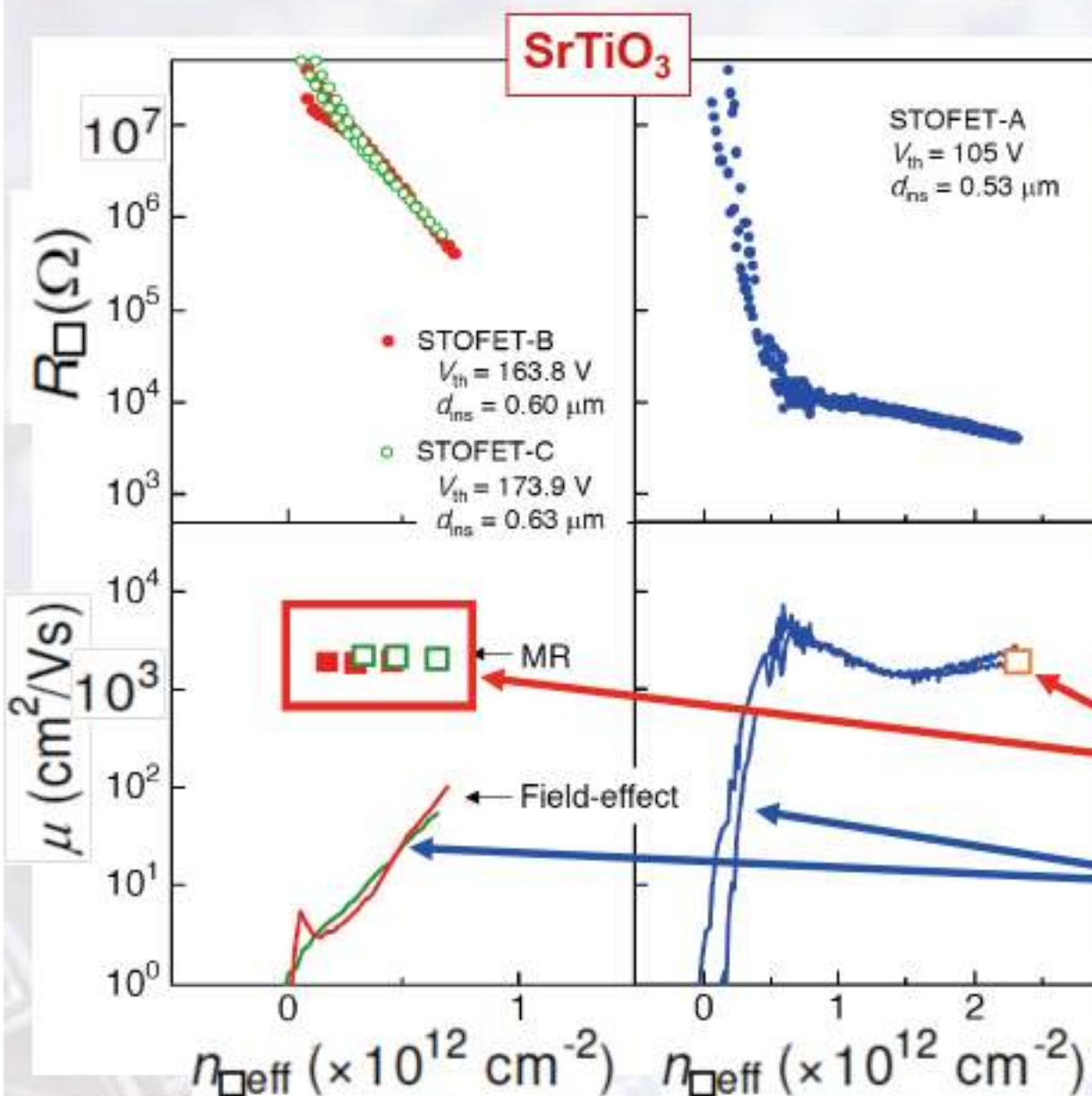


Figure 3. Sketches of (a) a high-field domain, and (b) a high-current filament, where  $J$  is the current density and  $\mathcal{E}$  is the electric field.



# Percolation Model of MI transition

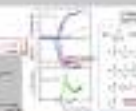
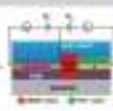
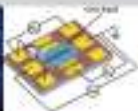


$$\mu_{\text{MR}} = \frac{1}{B} \sqrt{\frac{\Delta R}{R_0}}$$

reflects highest mobility domain in the channel

reflects averaged mobility in the whole channel

$$\mu_{\text{FE}} = \frac{1}{e} \frac{\partial}{\partial n_{\square}} \left( \frac{1}{R_{\square}} \right)$$

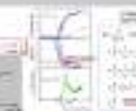
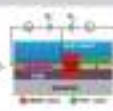
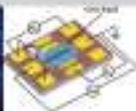
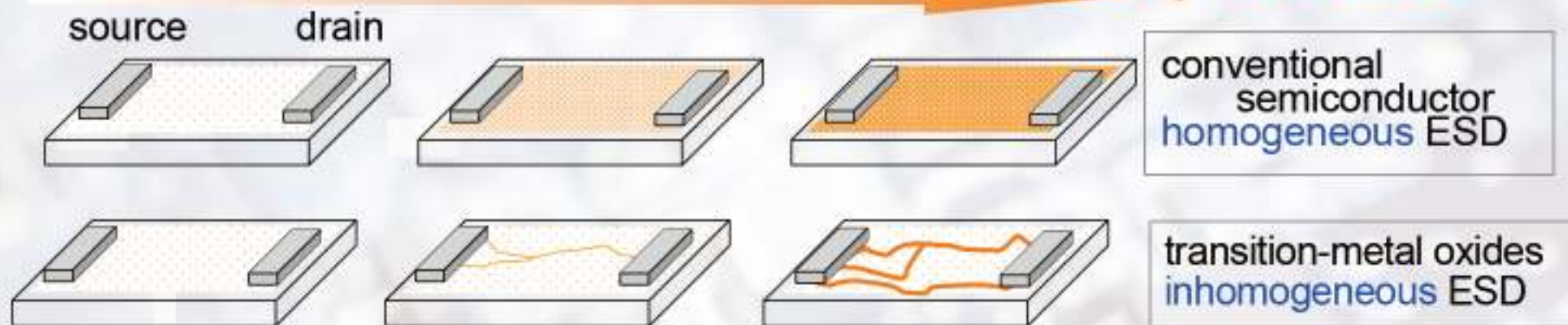




# Homo. Channel or Filaments

Electrostatic carrier doping (ESD)

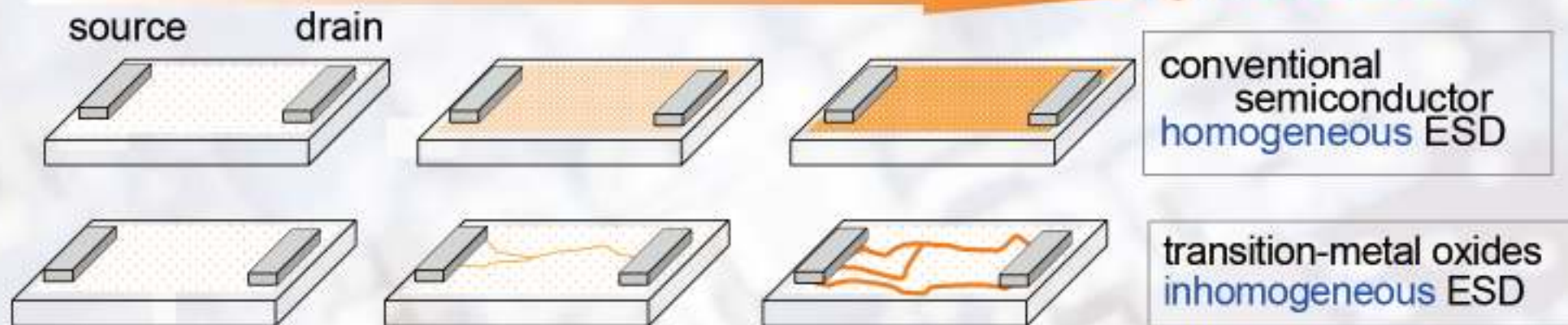
$V_G$  increased



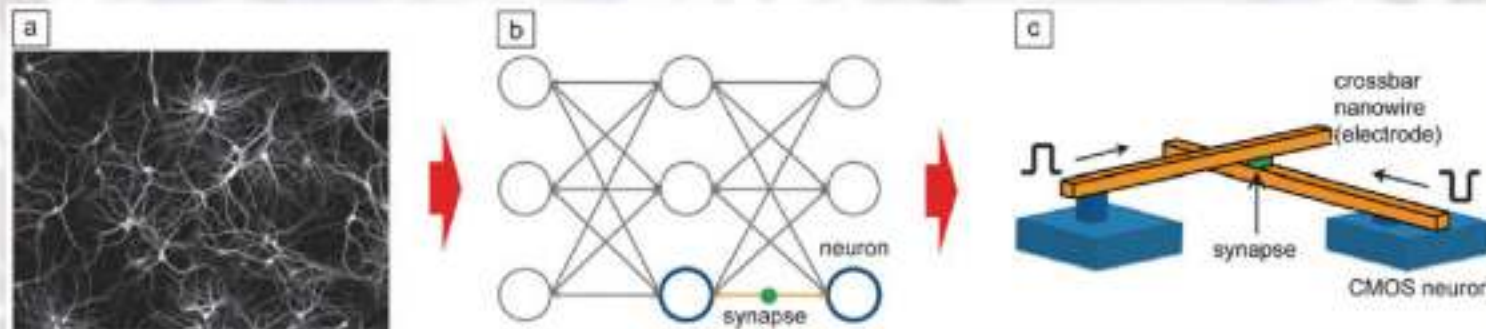
# Homo. Channel or Filaments

Electrostatic carrier doping (ESD)

$V_G$  increased



Main topic of resistive switching memory — adaptive electronics



D.B.Strukov and  
H. Kohlstedt,  
MRS Bulletin **37**,  
108 (2012)

